

AMENDMENTS TO THE SPECIFICATION:

Please amend the paragraph appearing at page 6, lines 12-16, to read as follows:

--Supplementary ASIC 3 has a 5V-analog/digital converter 3a having eight channels (ADC channels), an SPI interface 3b, two low-signal output stages 3c with a diagnosis device 3d and a driver 3e for bi-directional serial interfaces (ISO driver). Furthermore, the set of peripheral chips may have two lambda sensors 31, 32 (lambda evaluation) as optional expansion.--.